Fig. 1a

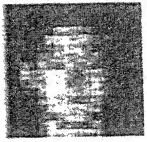

Fig. 1b

Fig. 1c

Fig. 1d

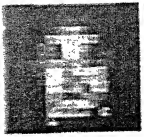

Fig. 1e

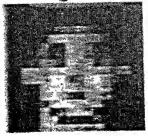

Fig. 1f

Fig. 1g

Fig. 1h

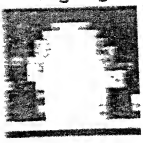

Fig. 1i

Fig. 1j

10056025.012802

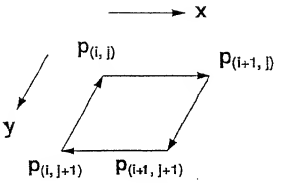

Fig. 2R

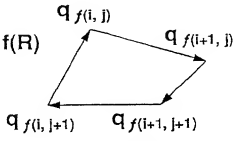

Fig. 2A

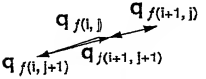

Fig. 2E

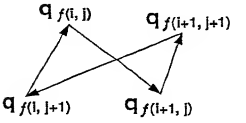

Fig. 2B

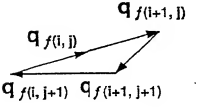

Fig. 2D

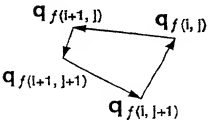

Fig. 2C

1005025-042803

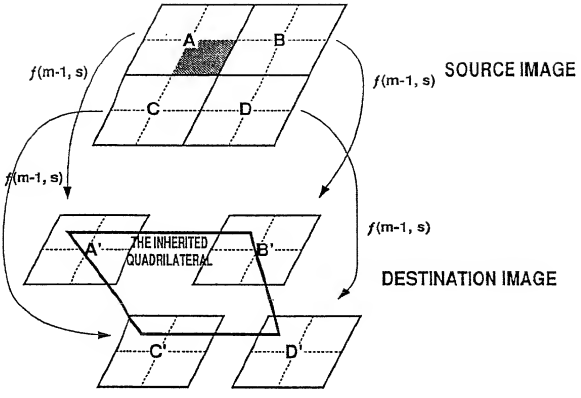
A
B
C
D
f(m-1, s)
f(m-1, s)
SOURCE IMAGE
f(m-1, s)
f(m-1, s)
A'
THE INHERITED QUADRILATERAL
B'
DESTINATION IMAGE
C'
D'

Fig. 3

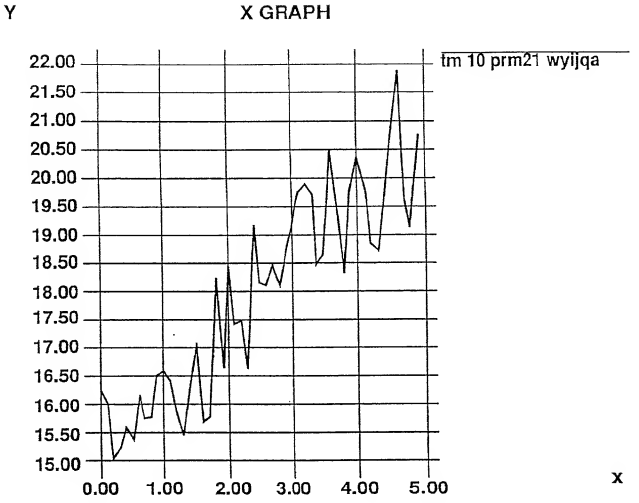

Fig. 4

1005025.012502

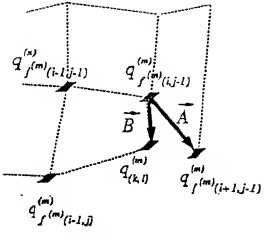

Fig. 5a

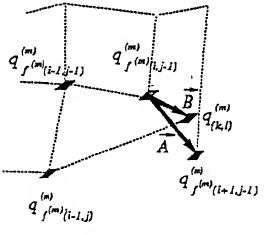

Fig. 5b

10050025-012802

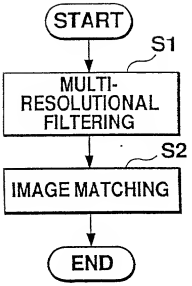

Fig. 6

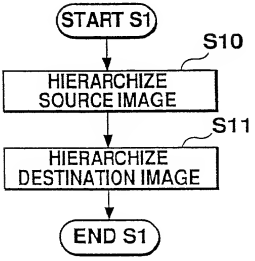

Fig. 7

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10056025.012202

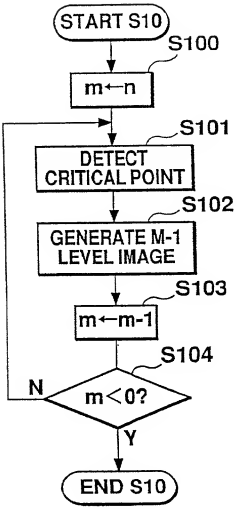
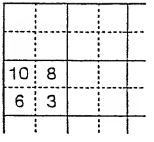


Fig. 8

1005025.012602



p(m, s)

p(m-1, 0)

p(m-1, 1)

p(m-1, 2)

p(m-1, 3)

Fig. 9

1005025.012802

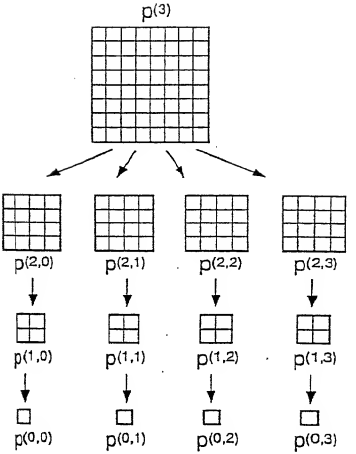


Fig. 10

1005025-012802

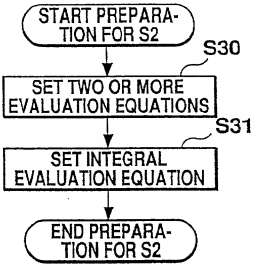


Fig. 11

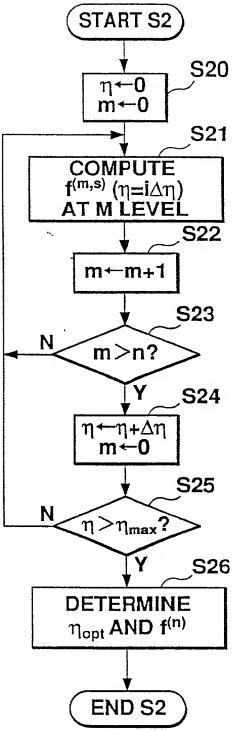

START S2
S20
η←0
m←0
S21
COMPUTE
f(m,s) (η=iΔη)
AT M LEVEL
S22
m←m+1
S23
N
m>n?
Y
S24
η←η+Δη
m←0
S25
N
η>ηmax?
Y
S26
DETERMINE
ηopt AND f(η)
END S2

Fig. 12

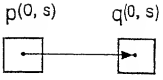


Fig. 13

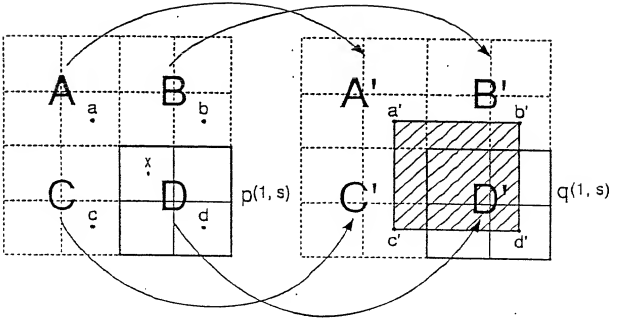


Fig. 14

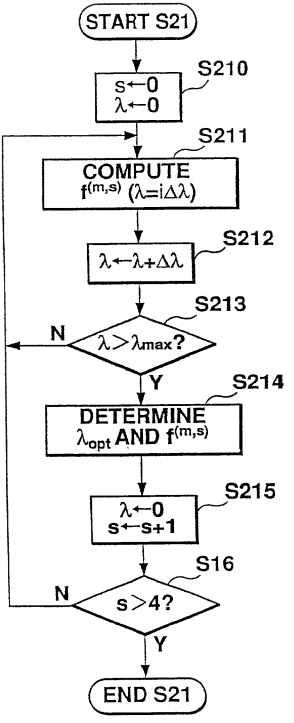

START S21
S210
s←0
λ←0
S211
COMPUTE
f(m,s) (λ=iΔλ)
S212
λ←λ+Δλ
S213
N
λ>λmax?
Y
S214
DETERMINE
λopt AND f(m,s)
S215
λ←0
s←s+1
S16
N
s>4?
Y
END S21

Fig. 15

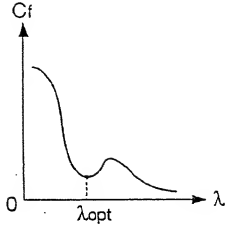


Fig. 16

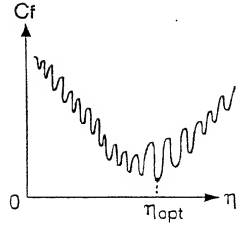


Fig. 17

10056025.012802

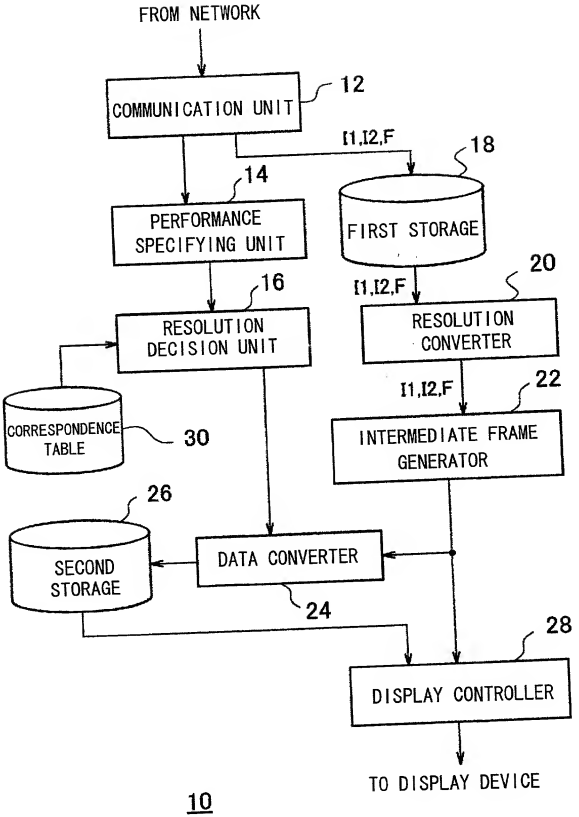

FROM NETWORK
COMMUNICATION UNIT
12
I1,I2,F
18
14
PERFORMANCE SPECIFYING UNIT
FIRST STORAGE
20
I1,I2,F
16
RESOLUTION DECISION UNIT
RESOLUTION CONVERTER
I1,I2,F
22
CORRESPONDENCE TABLE
30
INTERMEDIATE FRAME GENERATOR
26
SECOND STORAGE
DATA CONVERTER
24
28
DISPLAY CONTROLLER
TO DISPLAY DEVICE
10

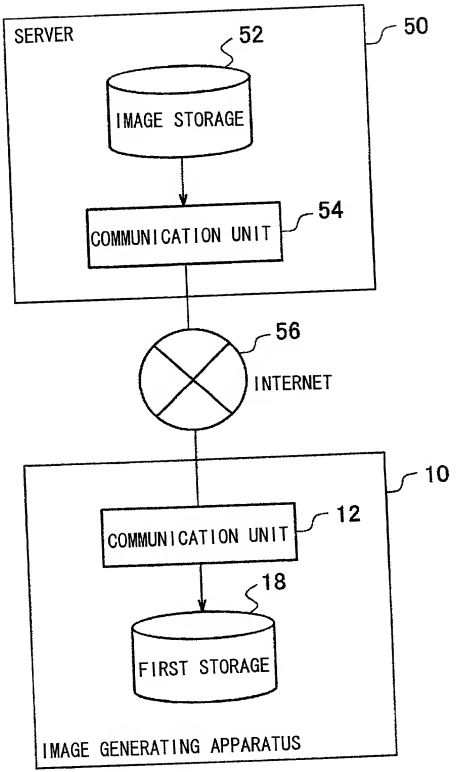
Fig.18

2002-10-S2095001

100 GRAPHIC ACCELERATOR	102 CPU	104 CLOCK	106 RESOLUTION	108 RANK
1	—	—	r≦1200×1200	A
0	○○○	750≦CL	r≦1200×1200	
	△△△	600≦CL	r≦1200×1200	
	○○○	400≦CL≦750	r≦600×600	B
	△△△	250≦CL≦600	r≦600×600	
	≀	≀	≀	
	○○○	CL≦80	MOVIE FILE	X
	△△△	CL≦60	MOVIE FILE	
	OTHERS	—	MOVIE FILE	

30

Fig.19


SERVER
52
50
IMAGE STORAGE
COMMUNICATION UNIT
54
56
INTERNET
10
COMMUNICATION UNIT
12
18
FIRST STORAGE
IMAGE GENERATING APPARATUS

200

Fig.20